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APPLICANTS

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** CONTINUING DATA ****
Z. K.

This application is a DIV of 09/625,643 07/25/2000

** FOREIGN APPLICATIONS ****
Z. K.

JAPAN 11-213098 07/28/1999
 JAPAN 11-213123 07/28/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 11/10/2003

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	DRAWING 11	CLAIMS 26	CLAIMS 2
Verified and Acknowledged Examiner's Signature <i>Z. K.</i>	Initials				

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TITLE

Semiconductor integrated circuit device with enhanced resistance to electrostatic breakdown

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input checked="" type="checkbox"/> 1.18 Fees (Issue)
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